

Notice of References CitedApplication/Control No.
10/004,976Applicant(s)/Patent Under
Reexamination
RATEGH ET AL.Examiner
Leigh Marie GarbowskiArt Unit
2825

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	I	US-			
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	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Berkcan et al., "Physical Assembly for Analog Compilation of High Voltage ICs," IEEE 1988 Custom ICs Conference, pages 14.3.1-14.3.7.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.